

Applicant: Hofmeister, et al.

Serial No.: 10/695,342

Att'y Docket No.: 15436.253.66.1

Filing Date: October 28, 2003

Group: 2828

For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT  
AND METHOD FOR FIBER OPTICS DEVICEINFORMATION DISCLOSURE CITATIONS MADE BY APPLICANTU.S. Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>
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<u>  </u> 2	4,378,451	03/29/1983	Edwards
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<u>  </u> 7	4,916,707	04/10/1990	Rosenkranz
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<del>19</del>	5,516,563	05/14/1996	Schumann et al.
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<del>22</del>	5,594,748	01/14/1997	Jabr
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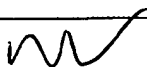
For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT  
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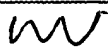
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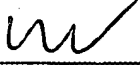
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- 117 Documentation entitled "IR Receiver for Data Communication: U2538B", copyright 2003 by Atmel Corporation
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- 120 *LXT16706/16707 SerDes Chipset*, Intel Products, [www.intel.com/design/network/products/optical/phys/1xt16706.htm](http://www.intel.com/design/network/products/optical/phys/1xt16706.htm), April 19, 2002.
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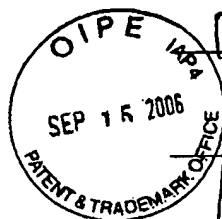
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- 126 Fairchild Semiconductor, Application Note 77, *CMOS, the Ideal Logic Family*, January 1983.
- 127 Analog Target Specification, Annex 48B, Published by IEEE New York, 05/2001, pp. 6-14.

### References Cited by Applicants

While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

Examiners will consider all citations submitted in conformance with 37 C.F.R. § 1.98 and MPEP Sec. 609 and place their initials adjacent the citations in the spaces provided on this form. Examiners will also initial citations not in conformance with the guidelines which may have been considered. A reference may be considered by the Examiner for any reason whether or not the citation is in full conformance with the guidelines. A line will be drawn through a citation if it is not in conformance with the guidelines AND has not been considered. A copy of the submitted form, as reviewed by the Examiner, will be returned to the applicant with the next communication. The original of the form will be entered into the application file.

Each citation initialed by the Examiner will be printed on the issued patent in the same manner as references cited by the Examiner on Form PTO-892.

The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

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